THE UNITED STATES PATENT AND TRADEMARK OFFICE

(Case No. 213.007-US)

In the Application of: YE ET AL.

Serial No: 10/815,573

Filed: APRIL 1, 2004

Title: SYSTEM AND METHOD OF LITHOGRAPHY

SIMULATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Group Art Unit: 1756

Before Examiner:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 10, 2005

FIFTH INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Submitted herewith is one (1) sheet of a modified Form PTO-1449. A copy of each document cited on the attached Form PTO-1449 is also submitted.

It is respectfully requested that the Examiner make his/her consideration of these references formally of record with the initial Office Action.

Respectfully submitted,

Date: January 10, 2005

Neil A. Steinberg, Reg. No. 34,735

Telephone No. 650-968-8079

JAN 1 2 2005 Sheet 1 of 1 SERIAL NUMBER ATTY. DOCKET NO. 10/815,573 213.007-US U.S. DEPARTMENT OF CO COMMERCE APPLICANT(S) PATENT AND TRADEMARK OFFICE Ye et al. FILING DATE FILING DATE INFORMATION DISCLOSURE STATEMENT 1756 April 1, 2004 BY APPLICANT

| | | υ. | S. PATENT DOCUMENTS | | | |
|----------|---------------------|---------|---------------------|-------|--------------|----------------|
| EXAMINER | DOCUMENT | DATE | NAME | CLASS | SUB CLASS | FILING DATE |
| INITIALS | NUMBER 5,563,702 | 10/1996 | Emery et al. | | | |
| | 5,737,072 | 4/1998 | Emery et al. | | | |
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| | | FO | REIGN PATENT DOCUMENTS | | | TRANSLATION |
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| EXAMINER | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB CLASS | YES/NO |
| INITIALS | NUMBER | | | | | |
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, 2007) |
|---|---|
| | "Photomask Production Integration of KLA STARlight" 3000 System, Raik et al., 1905, 1907, |
| | "Defect Detection and Classification in VLSI Pattern Inspection", Soo-lk Chae, September 1987, Ph.D. Dissertation |
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| | DATE CONSIDERED |
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| EXAMINER | |
| | through citation if not in |
| | - through citation if not in |

EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant.